# Analysis of Exchanging Interactions in Some Parallel Algorithms of the Linear Equational System 

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#### Abstract

Key Words: Parallel algorithm; data exchange; exchanging interactions. Abstract. This paper analyses and minimizes the exchanging interactions in the parallel algorithm of type „allocation of submatrices with identical dimensions in the processors of computer system with parallel architecture" of the linear equational system, solved by the method of sequential interations.


## 1. Introduction

An effective method for accelerating the solving a given task is to use a solution based on parallel algorithms (PA). There is a growing interest in searching and finding PA for solving complex problems in the last years [1,2,5]. PA presents the task as a set of simultaneously executed subtasks which exchange data throughout the process of solving. It is preferable these data to be minimal $[3,4]$, because in this way the traffic in the communicational network is decreased and, in the common case, the control of the parallel process is facilitated.

## 2. Aims

The aim of this article is: to analyze and minimize the exchanging interactions in PA of type „allocation of submatrices with identical dimensions in the processors of computer system (CS) with parallel architecture" of the linear equational system, solved by the method of sequential interations. (The essence of this PA is presented below. It can be referred to the so-called parallel matrix computations with checkerboard partitioning [1].) In details, the aim of this paper (of the presented task) will be:

1) determining the number of transmitted and received words between the threads (the branches) of the PA;
2) analysing the possible allocations of the matrix elements $\boldsymbol{\alpha}$ (see pt. 3) and suggesting allocations, in which the algorithms for routing of the exchanged words are simple and the number of the words - transmitted to and received in the parallel branches is minimal;
3) analysing the dependence of the number of words - transmitted to and received in the parallel branches on the submatrix dimension, aiming the determination of dimension, in which this number is minimal.
The Solution of the problems, presented above, will be searched considering the following conditions:
4) the matrices of the linear equational system do not containzeros;
2)the congruity of the solution is acceptable.

This paperhas an even further aim: searching forethfor analysis of the exchanging interactions in PA of complex tasks for finding optimal PA (according to defined criteria) and optimal topologies for communication between the parallel branches of the PA.

## 3. Method of Sequential Iterations for Solving the Linear Equational System

Let us have the linear equational system:

$$
\begin{aligned}
& a_{11} x_{1}+a_{12} x_{2}+\ldots+a_{1 n} x_{n}=b_{1} \\
& a_{21} x_{1}+a_{22} x_{2}+\ldots+a_{2 n} x_{n}=b_{2} \\
& \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots \\
& a_{n 1} x_{1}+a_{n 2} x_{2}+\ldots+a_{n n} x_{n}=b_{n} .
\end{aligned}
$$

In matrix form: $\mathbf{A X}=\mathbf{B}$, where

$$
\begin{aligned}
& \mathbf{A}=\left|\begin{array}{cccc}
a_{11} & a_{12} & \ldots & a_{1 n} \\
a_{21} & a_{22} & \ldots & a_{2 n} \\
\ldots & \ldots & \ldots & \ldots \\
a_{n 1} & a_{n 2} & \ldots & a_{n n}
\end{array}\right| ; \quad X=\left|\begin{array}{l}
x_{1} \\
x_{2} \\
\ldots \\
x_{n}
\end{array}\right| ; \quad \mathbf{B}=\left|\begin{array}{l} 
\\
b_{1} \\
b_{2} \\
\ldots \\
b_{n}
\end{array}\right| \\
& \text { If } \alpha_{i j} \neq 0, \quad i,=1,2, \ldots n \text {, then } \\
& x_{1}=\beta_{1}+\alpha_{11} x_{1}+\alpha_{12} x_{2}+\ldots+\alpha_{1 n} x_{n} ; \\
& x_{2}=\beta_{2}+\alpha_{21} x_{1}+\alpha_{22} x_{2}+\ldots+\alpha_{2 n} x_{n} \text {; } \\
& x_{n}=\beta_{1}+\alpha_{n 1} x_{1}+\alpha_{n 2} x_{2}+\ldots+\alpha_{n n} x_{n} ; \\
& \beta_{i}=\frac{b i}{a i i}, \alpha_{i j}=-\frac{a i j}{a i i} \text { if } i \neq j,\left(\alpha_{i j} \neq 0\right) \text {, } \\
& \alpha_{i j}=0 \text { if } i=j, \quad i, j=1,2, \ldots n \text {. }
\end{aligned}
$$

In matrix form: $X=\beta+\alpha X$
The column of free members is considered for zero approximation: $X^{(0)}=\beta$. After that, the matrix-columns are computed $X^{(1)}=\beta+\alpha X^{(0)}$ (first approximation), $X^{(2)}=\beta+\alpha X^{(1)}$ (second approximation) and so on. Each approximation is calculated according to the formula $X^{(k+1)}=\beta+\alpha X^{(k)}, \kappa=1,2, \ldots$.

If the order $\boldsymbol{X}^{(0)}, \boldsymbol{X}^{(1)}, \boldsymbol{X}^{(2)}, \ldots \boldsymbol{X}^{(k)}, \ldots$ has a limit $\boldsymbol{X}=\lim _{k \rightarrow \infty}$ $X^{(k)}$, this limit is solution of the system. Practically, when finding an approximation $\boldsymbol{X}_{i}^{(k)} \boldsymbol{i}=\mathbf{1}, \mathbf{2}, \ldots \boldsymbol{n}$ the difference is calculated
$\Delta X_{i}^{(k)}=X_{i}^{(k)}-X_{i}^{(k-1)}$. The task is considered solved when the condition $\Delta X_{i}^{\left({ }_{(N)} \ll \varepsilon\right.}$ is satisfied, where $i=1,2, \ldots n$ and $\varepsilon>0 . \varepsilon$ is a given small number, defining the calculation precision.

Solving the task includes $\mathbf{n}^{2}$ multiplications, $\mathrm{n}^{2}$ sums, $\mathbf{n}$ subtractions and $\mathbf{n}$ comparisons in one iteration cycle. It can be seen that the great part of computing work includes multiplications ( $\mathrm{n}^{2}$ ) and summings $\left(\mathrm{n}^{2}\right)$.

## 4.Exchanging Interactions in PA of Type "Checkerboard Partitioning" of the Linear Equational System, Solved by the Method of Sequential Iterations

Let the number of processors be $\mathbf{m}$. The contemporary CS with parallel architecture are built of dozens, hundreds and, sometimes, even thousands of processor elements [1]. In this paper, it will be considered that $\mathbf{m}=\mathbf{n}$. It will be shown this does not influence the generality of the considerations.

In the checkerboard partitioning, the matrix $\alpha$ is split in n submatrices with identical dimensions (or dimensions that differ with 1). For facilitating the presentation, it is assumed that the submatrices are identical. Let the number of rows of the submatrix be $\mathbf{r}$, and the number of columns be $\mathbf{c}$. It is not difficult to see that $\mathbf{r} \mathbf{c}=\mathbf{n}$. The number of the submatrices in each row of the checkerboard partitioning is $\mathbf{r}$, and their number in each column is $\mathbf{c}$. When $\mathbf{r}=1$ the partitioning is block-rowed $(\mathbf{1 \times n})$, and when $\mathrm{c}=1$ - it is block columned $(1 \times \mathrm{n})$ [1].

The values of each submatrix are allocated (saved) in one of the processors of CS. Each processor calculates one $\mathbf{x}_{\mathbf{i}}$, $\mathbf{i}=1,2, \ldots, n$. The Processor $\mathbf{P}_{\mathrm{i}}$ will be considered to calculate $\mathbf{x}_{\mathrm{i}}$. It is obvious that value of $\beta$, has to be saved in the processor $P_{i}$. The allocation of submatrices with dimension $2 \times 3$ in processors of CS $(\mathbf{n}=\mathbf{6})$ is shown in figure 1.

The number of possible allocations of submatrices in the processors is $\mathrm{n}!$. The allocation of submatrices, itself, defines the quantity of transmitted and received words between the parallel branches of PA and the loading of the communicational network.

| $\frac{\mathbf{P}_{1}-\mathbf{x}_{1}}{\mathrm{~b}_{1}}$ | $\begin{aligned} & a_{11}, a_{12}, a_{13} \\ & a_{21}, a_{22}, a_{23} \end{aligned}$ | $\frac{\mathrm{P}_{2}-\mathrm{X}_{2}}{\mathrm{~b}_{2}}$ | $\begin{aligned} & \mathrm{a}_{14,}, \mathrm{a}_{15}, \mathrm{a}_{16} \\ & \mathrm{a}_{24}, \mathrm{a}_{25}, \mathrm{a}_{26} \\ & \hline \end{aligned}$ |
| :---: | :---: | :---: | :---: |
| $\frac{P_{3}-x_{3}}{b_{3}}$ | $\begin{aligned} & a_{31}, a_{32}, a_{33} \\ & a_{41}, a_{42}, a_{43} \end{aligned}$ | $\underline{P}_{4}-x_{4}$ | $\begin{aligned} & a_{34,} a_{35}, a_{36} \\ & a_{44}, a_{45}, a_{46} \\ & \hline \end{aligned}$ |
| $\frac{P_{5}-x_{5}}{b_{5}}$ | $\begin{aligned} & a_{51}, a_{52}, a_{53} \\ & a_{61}, a_{62}, a_{63} \end{aligned}$ | $\mathbf{P}_{6}-\mathbf{x}_{6}$ | $\begin{aligned} & a_{54}, a_{55}, a_{56} \\ & a_{64}, a_{65}, a_{60} \end{aligned}$ |

Figure 1. P-type allocation of submatrices (dimension $2 \times 3, n=6$ )

Each processor $\mathbf{P}_{\mathrm{i}}$ in CS:

1) calculates $n$ multiplications;
2) executes ( $\mathbf{c}-1$ )r summings and, in result, $\mathbf{r}$ partial sums are calculated;
3)transmits $\mathbf{r}$ or $\mathbf{r}-1$ partial sums respectively to $\mathbf{r}$ or $\mathbf{r}-1$ processors, and receives $\mathbf{r}$ or $\mathbf{r}-1$ partial sums respectively from r or r-1 processors;
3) executes $\mathbf{r}-1$ summings of the $\mathbf{r}$ partial sums in the processor and, in result, $\mathbf{x}_{\mathrm{i}}$ is calculated;
4) if the parallel process is over - end, otherwise the 6-th step of the algorithm is executed for the completion of the next iteration;
5) $\mathbf{x}_{\mathbf{i}}$ is transmitted to $\mathbf{c}$ or $\mathbf{c - 1}$ processors, receives $\mathbf{c}$ or $c-1 x_{k j} j=1,2, \ldots, c-1$ or $j=1,2, \ldots, c ; k j \in\{1,2, \ldots, i-1, i+1, \ldots, n\}$ from $\mathbf{c}$ or $\mathbf{c - 1}$ processors respectively and goes on with the 1 st step of the algorithm.

The control of the calculation process (the 5-th step) is not a point to be discussed in this article. It is presented in $[3,4]$.

More attention will be paid to the 3 -rd and the 6 -th step, which define the exchanging interactions between the PA branches, i.e. between the processors of CS.

Each processor $\mathbf{P}_{\mathbf{i}}$ has to transmit $\mathbf{r}$ or $\mathbf{r - 1}$ partial sums respectively to $\mathbf{r}$ or $\mathbf{r} \mathbf{- 1}$ processors, and to receive $\mathbf{r}$ or $\mathbf{r} \mathbf{- 1}$ partial sums respectively from $\mathbf{r}$ or $\mathbf{r - 1}$ processors (the 3-rd step). It is obvious that the case transmit/receive $\mathbf{r}$-1 partial sums is preferable. This case is an allocation of submatrices, when one of the calculated $\mathbf{r}$ partial sums is in the $\mathbf{P}_{\mathbf{i}}$ processor and participates in the calculation of $\mathbf{x}_{\mathrm{i}}$. Therefore, such an allocation of submatrices has to be done that each processor in the CS $\mathbf{P}_{\mathrm{i}}, \mathbf{i}=1,2, \ldots, \mathrm{n}$ calculates one of the partial sums necessary for the computation of its $\mathbf{x}_{\mathrm{i}}$. Such allocation will be called $\mathbf{P}$-type allocation.

Rule 1. A P-type allocation is achieved when the index of each processor $\mathbf{P}_{\mathrm{i}}$, computing $\mathbf{x}_{\mathrm{i}}, \mathbf{i}=\mathbf{1}, \mathbf{2}, \ldots, \mathbf{n}$, coincides with one of the $\mathbf{r}$ row-numbers of the submatrix, allocated in this processor. In other words, each of the $\mathbf{r}$ processors in each row of the checkerboard partitioning computes one of these $\mathbf{x}_{\mathrm{i}}$, whose indices are the $\mathbf{r}$ row-numbers of the submatrices, allocated in the r processors.

The allocation in figure 1 complies with this condition. The number of all such allocations is $(\mathrm{r}!)^{c}$.

In this case the number of the transmitted $\mathbf{S}_{\mathrm{p}}$ and received $\mathbf{R}_{\mathrm{p}}$ words (partial sums) to and from each processor $\mathbf{P}_{\mathrm{i}}$ through the communicational network is:

$$
S_{p}=R_{p}=r-1
$$

The total number of transmitted $\mathbf{S}_{\mathrm{pt}}$ and received $\mathbf{R}_{\mathrm{pt}}$ words (partial sums) to and from all processors through the communicational network is:

$$
S_{\mathrm{pt}}=\mathrm{R}_{\mathrm{pt}}=\mathrm{n}(\mathrm{r}-1) .
$$

Attention should be poid to the fact that through the exchange of partial sums the set of processors $\boldsymbol{n}$ is split to $\mathbf{c}$ independent subsets, each of them containing $r$ processors (these in the rows of the checkerboard partitioning). This makes the routing algorithm simpler.

After the $\mathbf{k}$-th approximation of $\mathrm{x}_{\mathrm{i}}, \mathbf{i}=1,2, \ldots, \mathrm{n}$ is calculated (the 4-th step), if the parallel computing process has not finished, a new iteration cycle starts. For this purpose, the CS processors have to exchange the $\mathbf{k}$-th approximations of $\mathbf{x}_{\mathrm{i}}$. Each processor $\mathbf{P}_{\mathbf{i}}$ has to transmit the calculated by it $\mathbf{k}$-th approximation of $\mathbf{x}_{\mathrm{i}}$, to $\mathbf{c}$ or $\mathbf{c - 1}$ processors and has to receive $\mathbf{c}$ or $\mathbf{c}$ 1 values of the $\boldsymbol{k}$-th approximations of $\mathbf{x}_{\mathrm{i}}$ (these that are necessary for the calculation of the n multiplications, defined from the submatrix in the processor) from $\mathbf{c}$ or $\mathbf{c - 1}$ processors respectively (the 6 -th step). The case transmit one value to $\mathbf{c - 1}$ processors and receive of $\mathbf{c - 1}$ values of the $\mathbf{k}$-th approximations of
$\mathrm{x}_{\mathrm{i}}$ is preferable. This case finds its place when the $\mathbf{k}$-th approximation of $\mathbf{x}_{i}$ is used in processor $\mathbf{P}_{i}$ for the calculation of $\mathbf{r}$ from the $\mathbf{n}$ multiplications of the $(\mathbf{k}+\mathbf{1})$-st approximation of $\mathbf{x}$. Therefore, such an allocation of the submatrices has to be made that the above-stated condition is complied with all the processors of CS. Such allocation will be called I-type allocation.

Rule 2. An I-type allocation is achieved when the index of each processor $\mathbf{P}_{\mathrm{i}}$, computing $\mathbf{x}_{\mathrm{i}}, \mathbf{i}=1,2, \ldots, \mathbf{n}$, coincides with one of the $\mathbf{c}$ column-numbers of the submatrix that is allocated in this processor. In other words, each of the c processors in each column of the checkerboard partitioning computes one of these $\mathrm{x}_{\mathrm{i}}$, whose indices are the $\mathbf{c}$ column-numbers of the submatrices, allocated in the c processors.

The allocation in figure 2 complies with this condition. The number of all such allocations is (c!)r.

|  | $\mathbf{x}_{1}, \mathbf{x}_{2} \mathbf{x}_{\mathbf{3}}$ | $\mathbf{x}_{\mathbf{4}} \mathbf{x}_{\mathbf{5}} \mathbf{x}_{6}$ |  |
| :--- | :--- | :--- | :--- |
| $\underline{\mathbf{P}_{1}-\mathbf{x}_{1}} \mathrm{~b}_{1}$ | $a_{11}, a_{12}, a_{13}$ | $\underline{\mathbf{P}_{4}-\mathbf{x}_{4}}$, | $a_{14}, a_{15}, a_{16}$ |
| $\mathrm{~b}_{4}$ | $a_{24}, a_{25}, a_{26}$ |  |  |
| $\underline{\mathbf{P}_{2}-\mathbf{x}_{2}}$, | $a_{21}, a_{22}, a_{23}$ | $a_{31}, a_{32}, a_{33}$ | $\underline{\mathbf{P}_{5}-\mathbf{x}_{5}}$, |
| $\mathrm{b}_{2}$ | $a_{34}, a_{35}, a_{36}$ |  |  |
| $\underline{\mathbf{P}_{3}-\mathbf{x}_{3}}$, | $a_{51}, a_{42}, a_{43}, a_{53}$ | $\underline{\mathbf{P}_{6}-\mathbf{x}_{6}}$, | $a_{44}, a_{45}, a_{46}$ |
| $\mathrm{~b}_{3}$ | $a_{55}, a_{56}$ |  |  |

Figure 2. I-type allocation of submatrices (dimension $2 \times 3, \mathrm{n}=6$ )

In this case, the number of the transmitted $\mathbf{S}_{\mathrm{i}}$ and received $\mathbf{R}_{\mathbf{i}}$ words (unknowns $\mathbf{x}_{\mathrm{i}}$ ) to and from each processor $\mathbf{P}_{\mathrm{i}}$ through the communicational network is:

$$
\mathrm{S}_{\mathrm{i}}=1 \text { (to } \mathrm{c}-1 \text { processors); } \mathrm{R}_{\mathrm{i}}=\mathrm{c}-1 .
$$

The total number of transmitted $\mathbf{S}_{\mathrm{it}}$ and received $\mathbf{R}_{\mathrm{it}}$ words (unknowns $\mathbf{x}_{\mathrm{i}}$ ) to and from all processors through the communicational network is:

$$
S_{i t}=n ; R_{i t}=n(c-1) .
$$

One should to the fact that through the exchange of the unknowns $\boldsymbol{x}_{\mathrm{i}}$ the set of processors $\boldsymbol{n}$ is split to $\mathbf{r}$ independent subsets, each of them containing c processors (these in the columns of the checkerboard partitioning). This makes the routing algorithm simpler.

The allocation in figure1 is not optimal regarding the exchange of the unknowns $\mathbf{x}_{\mathrm{i}}$. The processors $\mathbf{P}_{2}$ and $\mathbf{P}_{5}$ have to transmit the calculated by themselves values of $x_{2}$ and $x_{5}$, to $\mathbf{c}(3)$ processors: $\mathbf{P}_{2}$ to $\mathbf{P}_{1}, \mathbf{P}_{3}$ and $\mathbf{P}_{5}$, and $\mathbf{P}_{5}$ to $\mathbf{P}_{2}, \mathbf{P}_{4}$ and $\mathbf{P}_{6}$, and have to receive $\mathbf{c}(3)$ values of unknowns: $\mathbf{P}_{2}-\mathbf{x}_{4}, \mathbf{x}_{5}$ and $\mathbf{x}_{6}$, and $\mathbf{P}_{5}-\mathbf{x}_{1}, \mathbf{x}_{2}$ and $\mathbf{x}_{3}$.

The allocation in figure 2 is not optimal regarding the exchange of the partial sums. The processors $\mathbf{P}_{2}, \mathbf{P}_{3}, \mathbf{P}_{4}$ and $\mathbf{P}_{5}$ have to transmit partial sums to $\mathbf{r}(2)$ processors and have to receive partial sums from $\mathbf{r}(2)$ processors: $\mathbf{P}_{2}$ transmits to $\mathbf{P}_{3}$ and $P_{4}$, receives from $P_{1}$ and $P_{4} ; P_{3}$ transmits to $P_{5}$ and $P_{6}{ }^{3}$, receives from $P_{2}$ and $\mathbf{P}_{5} ; P_{4}$ transmits to $P_{1}$ and $P_{2}$, receives from $\mathbf{P}_{2}$ and $\mathbf{P}_{5} ; \mathbf{P}_{5}$ transmits to $\mathbf{P}_{3}$ and $\mathbf{P}_{4}$, receives from $\mathbf{P}_{3}$ and $\mathbf{P}_{6}$.

## 5. PA of Type „Allocation of Submatrices with Identical Dimensions of Rearranged by Rows and Columns Matrix"

A question arises: could it not be found an allocation of submatrices that would comply with both rules? If it could, that would be the best allocation because:

- the number of exchanged words would be minimal;
- the routing algorithms would be simpler.

Applying both rules simultaneously in increasing order of the row numbers and column numbers is apparently impossible, because the solutions are mutually exclusive. This can be done only if these numbers are rearranged. In processor $\mathrm{P}_{\mathrm{i}}$, computing $\mathbf{x}_{\mathrm{i}}$, there really have to be a submatrix, containing the number of a row and a column of the matrix, coinciding with the index i. In other words, the indices of the processors on a given row (column) in the checkerboard partitioning define the numbers of the rows (columns) of the submatrices, allocated in the processors on the row (column) of the checkerboard partitioning. The aforementioned defines the rules for finding the submatrices at a given checkerboard partitioning, i.e. the rules for rearranging of the rows and columns of the matrix. In this case „a submatrix" does not include only sequential rows and columns.

| Column <br> Row | $\begin{array}{\|llll} \hline 1 & 2 & 3 & 4 \\ 12 & 7 & 1 & 5 \end{array}$ | $\begin{array}{ll} \hline 6 & 7 \\ 3 & 9 \end{array}$ | 9 10 11 12 <br> 6 10 8 11 |
| :---: | :---: | :---: | :---: |
| $\begin{array}{rr}1 & 12 \\ 2 & 2 \\ 3 & 6\end{array}$ | $\mathrm{P}_{12}-\mathrm{X}_{12}$ | $\mathrm{P}_{2}-\mathrm{X}_{2}$ | $P_{6}-X_{6}$ |
| $\begin{array}{rr} 4 & 7 \\ 5 & 3 \\ 6 & 10 \end{array}$ | $\mathrm{P}_{7}-\mathrm{X}_{7}$ | $P_{3}-X_{3}$ | $P_{10}-X_{10}$ |
| $\begin{array}{ll}7 & 1 \\ 8 & 1 \\ 9 & 9 \\ 9 & 8\end{array}$ | $P_{1}-X_{1}$ | $\mathrm{P}_{9}-\mathrm{X}_{9}$ | $\mathrm{P}_{8}-\mathrm{X}_{8}$ |
| 10 5 <br> 11 4 <br> 12 11 | $P_{5}-X_{5}$ | $P_{4}-X_{4}$ | $P_{11}-X_{11}$ |

Figure 3. P-type and I-type allocation of submatrices (dimension $3 \times 4, \mathrm{n}=12$ )

A rearrangement of rows and columns of the matrix ( $\mathbf{n}=12$ ), is shown in figure 3. The proposed allocation of submatrices with dimension (3x4) between the processors of CS complies with both rules. It can be seen that the numbers of the rows (columns) of the matrix in each row (column) of the checkerboard partitioning are identical with the indices of the processors in this row (column). For example, they are $7,3,10$ in the second row, and 12, 7, 1, 5 in the first column. The order of the row (column) numbers of the matrix in the rows (columns) of the checkerboard partitioning does not matter. The number of the possible rearrangements of a given checkerboard partitioning is $(r!)^{c}(c!)^{r}$.

Let us look at the work of one of the processors $-\mathbf{P}_{9}$. It computes the following three partial sums:

$$
\begin{aligned}
& \alpha_{12} x_{2}+\alpha_{13} x_{3}+\alpha_{19} x_{9}+\alpha_{14} x_{4} \\
& \alpha_{92} x_{2}+\alpha_{93} x_{3}+\alpha_{99} x_{9}+\alpha_{94} x_{4} \\
& \alpha_{82} x_{2}+\alpha_{33} x_{3}+\alpha_{89} x_{9}+\alpha_{84} x_{4}
\end{aligned}
$$

The processor $\mathbf{P}_{9}$ transmits the first partial sum to $\mathbf{P}_{1}$, and the third one to $P_{8}$, i.e. to the processors that are in its row of the checkerboard partitioning. The second partial sum stays in processor $P_{9}$. It is necessary for the calculation of $x_{9}$. The other two partial sums, necessary for the computation of $x_{9}$ :

$$
\begin{aligned}
& \alpha_{9,12} x_{12}+\alpha_{97} x_{7}+\alpha_{91} x_{1}+\alpha_{95} x_{5} \\
& \alpha_{96} x_{6}+\alpha_{9,10} x_{10}+\alpha_{98} x_{8}+\alpha_{9,11} x_{11}
\end{aligned}
$$

The processor $\mathbf{P}_{9}$ receives from the processors $\mathbf{P}_{1}$ and $\mathbf{P}_{8}$ respectively. When $X_{9}$ is calculated, the processor $P_{9}$ transmits its value to processors $\mathbf{P}_{2}, \mathbf{P}_{3}$ and $\mathbf{P}_{4}$, i.e. to the processors that are in its column of the checkerboard partitioning.

The other 11 processors have the same work organization.

| follumn | $\begin{array}{\|cccc\|} \hline 1 & 2 & 3 & 4 \\ 1 & 4 & 7 & 10 \\ \hline \end{array}$ | $\begin{array}{cccc} \hline 5 & 6 & 7 & 8 \\ 2 & 5 & 8 & 11 \\ \hline \end{array}$ | 9 10 11 12 <br> 3 6 9 12 |
| :---: | :---: | :---: | :---: |
| $\begin{aligned} & 1 \\ & 2 \\ & 3 \end{aligned}$ | $P_{1}-X_{1}$ | $\mathrm{P}_{2}-\mathrm{X}_{2}$ | $P_{3}-X_{3}$ |
| 4 5 6 | $\mathrm{P}_{4}-\mathrm{X}_{4}$ | $\mathrm{P}_{5}-\mathrm{X}_{5}$ | $\mathrm{P}_{6}-\mathrm{X}_{6}$ |
| $\begin{aligned} & 7 \\ & 8 \\ & 9 \end{aligned}$ | $\mathrm{P}_{7}-\mathrm{X}_{7}$ | $\mathrm{P}_{8}-\mathrm{X}_{8}$ | $\mathrm{P}_{9}-\mathrm{X}_{9}$ |
| 10 11 12 | $\mathrm{P}_{10}-\mathrm{X}_{10}$ | $\mathrm{P}_{11}-\mathrm{X}_{11}$ | $\mathrm{P}_{12}-\mathrm{X}_{12}$ |

Figure 4. P-type and I-type allocation of submatrices with rearrangement only of the column numbers of the matrix (dimension $3 \times 4, n=12$ )

The allocation in figure 3 is incidentally chosen in order to illustrate the rearrangement of the numbers of rows and columns of the matrix. The most probable rearrangements are shown in figure 4 and figure 5.

The allocation in figure 4 complies with the conditions of Rule 1. That is why, in this case, only a rearrangement of the column numbers of the matrix is necessary.

The allocation in figure 5 complies with the conditions of Rule 2. That is why, in this case, only a rearrangement of the row numbers of the matrix is necessary.

| $\mathrm{row}_{\text {row }}^{\text {column }}$ | 23 | 6 | $9 \quad 10 \quad 11 \quad 12$ |
| :---: | :---: | :---: | :---: |
| $\begin{array}{ll}1 & 1 \\ 2 & 5 \\ 3 & 9\end{array}$ | $P_{1}-X_{1}$ | $P_{5}-X_{5}$ | $\mathrm{P}_{9}-\mathrm{X}_{9}$ |
| $\begin{array}{rr} \hline 4 & 2 \\ 5 & 6 \\ 6 & 10 \end{array}$ | $\mathrm{P}_{2}-\mathrm{X}_{2}$ | $P_{6}-X_{6}$ | $\mathrm{P}_{10}-\mathrm{X}_{10}$ |
| $\begin{array}{rr}7 & 3 \\ 8 & 7 \\ 9 & 11\end{array}$ | $P_{3}-X_{3}$ | $P_{7}-X_{7}$ | $P_{11}-X_{11}$ |
| $\begin{array}{cc}10 & 5 \\ 11 & 4 \\ 12 & 11\end{array}$ | $P_{4}-X_{4}$ | $\mathrm{P}_{8}-\mathrm{X}_{8}$ | $P_{12}-X_{12}$ |

Figure 5. P-type and I-type allocation of submatrices with rearrangement only of the row numbers of the matrix (dimension $3 \times 4, n=12$ )

It can be seen that the order of the rearranged numbers of the rows/columns is cyclic. For the rows (figure 5) the cycle is $4(\mathbf{c})$, and for the columns (figure 4) $-3(\mathbf{r})$.

An incidental allocation of the submatrices without rearrangement complicates the routing algorithms and the number of exchanged words in PA is not minimal.

## 6. Dependence of the Number of the Exchanged Words on the Dimension of the Submatrices of Allocation

The dependences of the number of the exchanged words for one branch of PA and for all branches of PA, go in pt.4, are shown in table 1.

A processor (branch) transmits least number of words when $\mathbf{r}=1$, and it receives least number of words when $\mathbf{r}=\mathbf{n}^{1 / 2}$. Practically, this means that $\mathbf{r}$ and $\mathbf{c}$ are necessary to be close by value in order a minimal number of received words to be achieved. For example, if $\mathbf{n}=900$, the processors will receive minimal number of words -58 , when $\mathbf{r}=\mathbf{c}=30$. If $\mathbf{n}=800$, because $800^{1 / 2} \approx 28,3$, i.e. it is not an integer, one good choice is $\mathbf{r}=25, \mathbf{c}=32$. In this case the number of received words is 55 .

The number of exchanged words (received and transmitted) is minimal when $\mathbf{r}=(\mathbf{n} / 2)^{1 / 2}$. Therefore, $\mathbf{c}=(2 \mathbf{n})^{1 / 2}$, i.e. $\mathbf{c} / \mathbf{r}=2$. For example, if $\mathbf{n}=800$, the best case is $\mathbf{r}=20, \mathbf{c}=40$. The number of exchanged words from each processor (branch) will be 78, 20 of which will be transmitted and 58 - received.

Table 1. Dependencies of the number of the exchanged words on the dimension of the submatrices

|  | For one branch of PA |  | For all branches of PA |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Transmitted words S | Received words $\mathbf{R}$ | Transmitted words $\mathbf{S}$ | Received words $\mathbf{R}$ |
| Partial sums | $S_{p}=\mathrm{r}-1$ | $\mathrm{R}_{\mathrm{p}}=\mathrm{r}-1$ | $\mathrm{S}_{\mathrm{pt}}=\mathrm{n}(\mathrm{r}-1)$ | $\mathrm{R}_{\mathrm{pt}}=\mathrm{n}(\mathrm{r}-1)$ |
| Unknowns $\mathbf{x}_{\text {i }}$ | $S_{i}=1$ | $\mathrm{R}_{\mathrm{i}}=\mathrm{c}-1=\mathrm{n} / \mathrm{r}-1$ | $S_{i t}=\mathbf{n}$ | $\mathrm{R}_{\mathrm{it}}=\mathbf{n}(\mathbf{n} / \mathrm{r}-1)$ |
| Total | $\mathrm{S}=\mathrm{r}$ | $\mathrm{R}=\mathrm{r}+\mathrm{n} / \mathrm{r}-2$ | $S_{t}=n \mathrm{r}$ | $\mathrm{R}_{\mathrm{t}}=\mathrm{n}(\mathrm{r}+\mathrm{n} / \mathrm{r}-2)$ |
| Total exchanged words | $\mathrm{S}+\mathrm{R}=2 \mathrm{r}+\mathrm{n} / \mathrm{r}-2$ |  | $S_{t}+R_{t}=n(2 r+n / r-2)$ |  |

The block-row partitioning and, especially, the block-column partitioning are the most unfavourable - the quantity of exchanged words is maximal (see figure 6) and each processor has to exchange (transmit and receive) words with each one of the other processors.

The dependences of the number of exchanged words on the number of the rows $(\mathbf{r})$ of the matrix of allocation for one branch of PA when $\mathbf{n}=\mathbf{1 6}$ is shown in figure 6 .

## 7. Avoiding the Restrictions in the Proposed PA

At the end, the two restrictions will be discussed:

- the number of the processors is equal to the number of the unknowns $\mathbf{x}_{\mathrm{i}}$, i.e. $\mathbf{m}=\mathbf{n}$ ( $\mathbf{m}, \mathbf{n}-$ integers);
- the dimension of the submatrices of allocation is identical.

The removing of the first restriction puts ahead the question for the following dependences.

1. $\mathrm{m}>\mathrm{n}$. This case is not very probable. (Solving tasks with

PA makes sense only when they involve a great number of operations, i.e. they are tasks with great dimensions.) The execution of PA of the task will engage $\boldsymbol{n}$ of the $\boldsymbol{m}$ processors, i.e. this will be the aforementioned case.
2. $\mathbf{n} \gg \mathrm{m}$. It will be considered that $\mathrm{n} / \mathrm{m}=\mathrm{t}$ is an integer. (This condition is connected with the second restriction, which will be discussed below.) In this case, each processor will compute the values of $\mathbf{t}$ unknowns $\mathbf{x}_{\mathrm{i}}$. Therefore,

$$
\mathrm{rc}=\mathrm{tn} ; \mathrm{r}, \mathrm{c} \geq \mathrm{t}
$$

In comparison with the case when $\mathbf{m}=\mathbf{n}$, the quantity of work for each processor increases $\mathbf{t}$ times.

The dependences of the number of exchanged words for one branch of PA and for all branches of PA is shown in table 2.

The number of exchanged words is minimal when $r=(\mathrm{tn} / 2)^{1 / 2}$.
3. $\mathbf{n}>\mathbf{m}>\mathbf{n} / \mathbf{2}$. This time it is appropriate PA of the task to be executed by $\mathrm{m}_{1}$ of the $\boldsymbol{m}$ processors $\left(\mathrm{m}_{1}<\mathbf{m}\right)$, where $m_{1} \leq n / 2$. There is maximal acceleration when $m_{1}=n / \mathbf{2}$. (It will be considered that $\mathbf{n} /\left(2 m_{1}\right)$ is an integer.)


Figure 6. Dependencies of the number of exchanged words on the number of the matrix rows $(\mathbf{r})$ for one branch of $P A(n=16)$

Table 2. Dependencies of the number of the exchanged words on the dimension of the submatrices ( $n \gg m, n / m=t$ - integer)


The removing of the second restriction puts ahead the need of the task analysis, which will be done in another article. Apparently, the restriction for identity of the dimensions of the submatrices can not be fulfilled by any $\mathbf{n}$ (for example, when $\mathbf{n}$ is a simple number), and in some cases it is far from the optimal solution (for example, when $\mathbf{n}=\mathbf{2 k}$, where $\mathbf{k}$ is a simple number).

The avoiding of the restriction (still keeping the advantages - simplicity of the routing algorithms and minimal word exchange) is based on the possibility for each $\mathbf{n}$, when ( $\mathbf{n} / 2)^{1 / 2}$ is not an integer, an allocation with rearrangement to be made, including submatrices with 4 dimenions: $\mathbf{r x c},(\mathbf{r}+1) \times \mathbf{c}, r \times(\mathbf{c}+1)$ and $(\mathbf{r}+1) \times(\mathbf{c}+1)$. The row (column) number of the submatrices in the rows (columns) of the checkerboard partitioning has to be identical.

Let $\mathbf{n}$ be given, where $(\mathbf{n} / \mathbf{2})^{1 / 2}$ is not an integer. The number of the processors $m$ and the dimension of the submatrices, i.e. $\mathbf{r}$ and $\mathbf{c}$, have to be defined. Using the condition for minimal exchange, $\mathbf{r}=](\mathbf{n} / \mathbf{2})^{1 / 2}[$ is computed. (The sign ][ is for the nearest smaller integer.) The integers $\boldsymbol{n}_{1}, \mathbf{c}$ and $\mathbf{n}_{2}$ are computed using respectively the dependences $\mathbf{r}=\left(\mathbf{n}_{1} / \mathbf{2}\right)^{1 / 2}, \mathbf{r} \mathbf{c}=\mathbf{n}_{1}$ and $\mathbf{r}+\mathbf{1}=\left(\mathbf{n}_{2} / \mathbf{2}\right)^{1 / 2}$. Apparently, $\mathbf{n}_{1}<\mathbf{n}<\mathbf{n}_{2}$. When $\mathbf{n}=\mathbf{n}_{1}$, optimal solution is achieved if the dimension of the submatrices is rxc, and when $\mathbf{n}=\mathrm{n}_{2}$ - if the dimension is $(\mathbf{r}+\mathbf{1}) \times(\mathbf{c}+\mathbf{2}) . \mathrm{n}_{12}=(\mathbf{r}+\mathbf{1}) \mathbf{c}=\mathbf{r}(\mathbf{c}+\mathbf{2})$ is computed. It is clear that $\mathrm{n}_{1}<\mathrm{n}_{12}<\mathrm{n}_{2}$.

Now the number of the processors m can be calculated. When $\mathrm{n}<\mathrm{n}_{12}, \mathrm{~m}=\mathrm{n}_{1}$. When $\mathrm{n}>\mathrm{n}_{12}, \mathrm{~m}=\mathrm{n}_{2}$. When $\mathrm{n}=\mathrm{n}_{12}, \mathrm{~m}=\mathrm{n}_{12}$.

In the first case, $\mathbf{q}=\mathbf{n}-\mathbf{n}_{1}$ of the $\mathbf{n}_{1}$ processors ( $\mathbf{q}<\mathbf{2 r}$ ) have to compute one more $\mathbf{x}_{\mathrm{i}}$, i.e. two unknowns $\mathbf{x}_{\mathrm{i}}$. This means that in $\mathbf{q}$ rows and $\mathbf{q}$ columns of the checkerboard partitioning the number of the submatrix rows of the checkerboard partitioning will increase with 1 , and the number of the submatrix columns - with 1 when $q \leq r$ and with 2 (when $q>r+1$ ) in comparison with submatrix of $\mathbf{n}_{2}$. Let the numbering of the unknowns in the checkerboard partitioning be sequential, horizontal. This means that only the columns of the matrix have to be rearranged. The processors ( $\mathbf{q}$ in number), that will compute two unknowns $\mathbf{x}_{\mathrm{i}}$, have to be one in a row and one (when $\mathbf{q} \leq \mathbf{r}$ ) or two (when $\mathbf{q}>\mathbf{r}$ ) in a column.

In the second case, $\mathbf{q}=\mathrm{n}_{2}-\mathrm{n}$ of the $\mathrm{n}_{2}$ processors $(\mathbf{q}<\mathbf{2 ( r + 1 ) )}$ do not have to compute $\mathbf{x}_{\mathrm{i}}$. This means that in $\mathbf{q}$ rows and $\mathbf{q}$ columns of the checkerboard partitioning the number of the submatrix rows will decrease with 1 , and the number of the submatrix columns - with 1 (when $\mathbf{q} \leq \mathbf{r}+1$ ) and with 2 (when $q>r+1$ ) in comparison with the submatrix of $n_{2}$. Let the numbering of the unknowns in the checkerboard partitioning be sequential, horizontal. This means that only the columns of the matrix have to be rearranged. The processors ( $\mathbf{q}$ in number), that will not compute $\mathbf{x}_{\mathrm{i}}$, have to be one in a row and one (when $\mathbf{q} \leq \mathbf{r}+1$ ) or two (when $q>r+1$ ) in a column.

The difference in the computing work of the processors at such an allocation will be as follows. The processors with dimension of the submatrices $(\mathbf{r}+1) \times \mathrm{c}$ calculate $\mathbf{c}$ multiplications and $\mathbf{c}-1$ sums more than the processors with dimension of the submatrices rxc. The processors with dimension of the submatrices $\mathbf{r x}(\mathbf{c}+1)-\mathbf{r}$ multiplications and $\mathbf{r}$ sums more. The processors with dimension of the submatrices $(\mathbf{r}+1) \times(\mathbf{c}+1)-$
$\mathbf{r}+\mathbf{c}-1$ multiplications and sums $\mathbf{r}+\mathbf{c}$ more. With the increasing of $\boldsymbol{n}$ the relative differences in the computation work of the processors will decrease. There is difference in the computation work of the processors also because of the differences in the number of the computed $\mathbf{x}_{\mathrm{i}}$. For each of them $\mathbf{r - 1}$ sums are calculated.

The dependences of the number of exchanged words for one branch of PA in table 1 and table 2 are actual for the proposed allocation. However, the number of exchanged words in PA is not this one. It is the sum of the exchanged words in all the parallel branches and, in this case, not all submatrices have identical $\mathbf{r}$ and $\mathbf{c}$.

Another allocations can be made, of course, but in the common case, they will not be optimal.

The allocation for $\mathbf{n}=\mathbf{1 1}$ is shown in figure 7 , and for $\mathbf{n}=\mathbf{1 4}$ - in figure 8. For both cases $\mathrm{n}_{1}=8, \mathrm{n}_{2}=18, \mathrm{n}_{12}=12$. When $n=11, n<n_{12}$. That is why $m=n_{1}=8$. The processors $P_{4}, P_{5}$ and $P_{8}(q=3)$ compute two unknowns $x_{i}$. When $n=14, n>n_{12}$. That is why $\boldsymbol{m}=\boldsymbol{n}_{2}=18$. The processors $\mathbf{P}_{9}, P_{10}, P_{14}$ and $P_{18}(q=4)$ do not compute $\mathbf{x}_{\mathrm{i}}$.

| Column | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Row | 1 | 3 | 6 | 7 | 9 | 2 | 4 | 5 | 8 | 10 | 11 |
| 1,2 | $\mathrm{P}_{1}-\mathrm{X}_{1}$ |  |  |  | $\mathrm{P}_{2}-\mathrm{X}_{2}$ |  |  |  |  |  |  |
| $3,4,5$ | $\mathrm{P}_{3}-\mathrm{X}_{3}$ | $\mathrm{P}_{4}-\mathrm{X}_{4}, \mathrm{X}_{5}$ |  |  |  |  |  |  |  |  |  |
| $6,7,8$ | $\mathrm{P}_{5}-\mathrm{X}_{6}, \mathrm{X}_{7}$ | $\mathrm{P}_{6}-\mathrm{X}_{8}$ |  |  |  |  |  |  |  |  |  |
| $9,10,11$ | $\mathrm{P}_{7}-\mathrm{X}_{9}$ | $\mathrm{P}_{8}-\mathrm{X}_{10}, \mathrm{X}_{11}$ |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |

Figure 7. Allocation of submatrices for $n=11$

| Column |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Row | 1 |  |  |  |  |  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $1,2,3$ | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 |
| 13 | 14 | 14 |  |  |  |  |  |  |  |  |  |
| $4,5,6$ |  | 4 | 7 | 11 | 13 | 2 | 5 | 8 | 9 | 14 | 3 |

Figure 8. Allocation of submatrices for $n=14$

## 8. Conclusion

In this article the exchanging interactions in PA of type „allocation of submatrices with identical dimensions in processors of CS with parallel architecture" of the linear equational system, solved through the method of sequential interations are analysed.

The number of transmitted and received words between the threads (branches) of PA is calculated.

Allocations of submatrices are suggested that facilitate the routing algorithms and minimize the number of exchanged words.
to be continued on page 39

As the results show, the LFU-RBH outperforms the other algorithms.

## Conclusions and Future Work

The conducted experiments show that the introduced improvements result very well in the behavior of the algorithm. Even without the newly proposed MRU section the LFU-RBH achieves very good hit ratio, in most cases better than some existing algorithms. Running with the MRU section gives a leading position of the LFU-RBH, compared with the experimented algorithms.

The use of data pooling results in a reduced utilization of the buffer memory. Also, the results for the different reference strings (RS1 and RS2) state that the LFU-RBH carries better the simulation of a large area scan situation.

Many other experiments can be conducted to tune the algorithm and to balance between different parameters - hash bits, the length of RB, the number of sections, the length of MRU section and etc.

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The dimension of the submatrix is determined that defines minimal number of exchanged words.

This article "hints" for future directions of work, as:

- analsying the other possible allocations of the information in the parallel branches of the given task;
- analysing the exchanging interactions in PA of other tasks in order to find optimal algorithms.


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